



ITW

PATENT
Customer No. 22,852
Attorney Docket No. 03327.2340-00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Yoshitaka SUGAWARA et al.) Group Art Unit: 2838
Application No.: 10/565,389) Examiner: Williams, Arun C.
Filed: January 23, 2006) Confirmation No.: 1931
For: INVERTER APPARATUS)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT

In response to the Notice of Non-Compliant Amendment mailed March 28, 2008, the period for response to which extends through April 28, 2008, Applicants hereby submit ten drawing sheets (Figures 1-8) each labeled "REPLACEMENT SHEET." Applicants submit that the drawings sheets meet the requirements of 37 CFR 1.121(d).

If any fees are necessary for the submission of these formal drawings, please charge our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: April 11, 2008

By: /David W. Hill/
David W. Hill
Reg. No. 28,220



REPLACEMENT SHEET

1/2

FIG. 1, FIG. 3

a... U-PHASE OUTPUT

b... V-PHASE OUTPUT

c... W-PHASE OUTPUT

d... INVERTER CONTROL CIRCUIT

e... OUTPUT VOLTAGE COMMAND SIGNAL

f... PWM PULSE GENERATING CIRCUIT

g... UP INITIAL SIGNAL

h... UN INITIAL SIGNAL

i... VP INITIAL SIGNAL

j... VN INITIAL SIGNAL

k... WP INITIAL SIGNAL

l... WN INITIAL SIGNAL

m... SIMULTANEOUS SWITCHING PREVENTION CIRCUIT

n... UP GATE SIGNAL

o... UN GATE SIGNAL

p... VP GATE SIGNAL

q... VN GATE SIGNAL

r... WP GATE SIGNAL

s... WN GATE SIGNAL

FIG. 5

a, b, c, d, e... HIGH-POSSIBILITY SIMULTANEOUS-SWITCHING POINTS



REPLACEMENT SHEET

2/2

FIG. 6

a... UP INITIAL SIGNAL

b... POST-TURNING-OFF Δt_1 , GENERATING CIRCUIT

c... POST-TURNING-ON Δt_2 GENERATING CIRCUIT

d... SIMULTANEOUS SWITCHING PREVENTION LOGIC CIRCUIT

e... DEAD TIME GENERATING CIRCUIT

f... UP GATE SIGNAL

g... UN GATE SIGNAL

h... UN INITIAL SIGNAL

i... VP INITIAL SIGNAL

j... VP GATE SIGNAL

k... VN GATE SIGNAL

l... VN INITIAL SIGNAL

m... WP INITIAL SIGNAL

n... WP GATE SIGNAL

o... WN GATE SIGNAL

p... WN INITIAL SIGNAL

FIG. 8

A... GATE DRIVE CIRCUIT